

CLEAN VERSION OF ABSTRACT**NETWORK TOPOLOGY FOR A SCALABLE MULTIPROCESSOR SYSTEM**

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Abstract of the Disclosure

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A system and method for interconnecting a plurality of processing element nodes within a scalable multiprocessor system is provided. Each processing element node includes at least one processor and memory. A scalable interconnect network includes

5 physical communication links interconnecting the processing element nodes in a cluster. A first set of routers in the scalable interconnect network route messages between the plurality of processing element nodes. One or more metarouters in the scalable interconnect network route messages between the first set of routers so that each one of the routers in a first cluster is connected to all other clusters through one or more

10 metarouters.